## Apr 4, 2024 10:00 AM | [Senior Design Team 50 Biweekly Meeting](https://www.google.com/calendar/event?eid=MnBuOTRzNTVvNDJwOW4xN240NmhmZG1pYWpfMjAyMzEyMDVUMTQwMDAwWiByYWJhaWxlM0BuY3N1LmVkdQ)

Attendees:

| Present [Huangjie Gong](mailto:huangjie.gong@us.abb.com)  Present [Andrew Bailey](mailto:rabaile3@ncsu.edu)  Present [Manny Harris](mailto:erharris@ncsu.edu) | Present [Ralph Cullom](mailto:rmcullom@ncsu.edu)  Absent [Labib Kasim](mailto:lkasim@ncsu.edu) |
| --- | --- |

[Team Charter - 50\_ResidentialPowerDisaggregation\_Fall\_2023](https://docs.google.com/spreadsheets/d/19QlSl8Cbm5M9cFBJFcICrvQb3tRDH6ZTBHSlsWt19BE/edit#gid=770004057)

Agenda:

* **Walk-in item solicitation**
* **Planning review**
  + Design Day Apr 19, 2024 8AM - 4PM
    - 15 days out
* **Beta Demo Review Comments**
  + ML model and code application/hardware location
  + Operating measurement outside of NCSU wifi
* **Previous action items & timeline check**
* **New Action Items**
  + Final project state and demonstration
    - Finalize metrics to show on grafana
      * Pie chart of load breakdown
      * Time series graph of total and individual loads
      * Look into to table or pie chart for cost display
    - ML model predictions for future power using weather
      * Not a priority
    - Other features/requirements
    - Live demo viewing data for design day
      * Poster
      * Grafana Demo
      * Slides
      * ML code
* **Walk in items**

Walk-in items:

* For demonstration purposes, is there any way to hard wire the CT board to an AD2 or plug in the ESP32 to get readings?
  + May be able to send over **SPI**?
    - Use waveforms logic analyzer, set the pins according to Github
    - Lower priority than ESPHome working on state wifi
* Is the project planning to be continued? Is there any documentation to focus on if so?
  + Improve documentation of previous group
  + Explanation of source code, comments in code how to use